

#2 IDS
S-29-01
R. H. H. H.

PATENT
TESSERA 3.0-109 CIP DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Kim, et al.

Application No.

Filed: HERewith

For: STACKED MICROELECTRONIC
ASSEMBLY AND METHOD THEREFOR

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Group Art Unit:
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Examiner:
:
Date: February 2, 2001

Assistant Commissioner for Patents
Washington, D.C. 20231

jc929 U.S. PTO
09/776356
02/02/01

INFORMATION DISCLOSURE STATEMENT

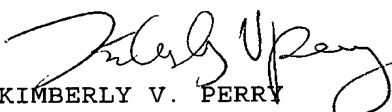
Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference was of record in Application No. 08/987,569 and Application No. 09/244,581, the benefit of which is claimed under §120.. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material. The Examiner's attention is respectfully directed to co-pending application 07/919,772, filed July 24, 1992.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Respectfully submitted,

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